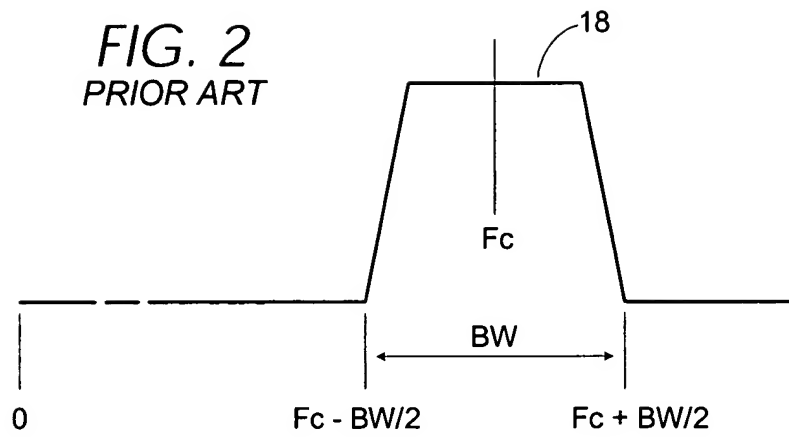
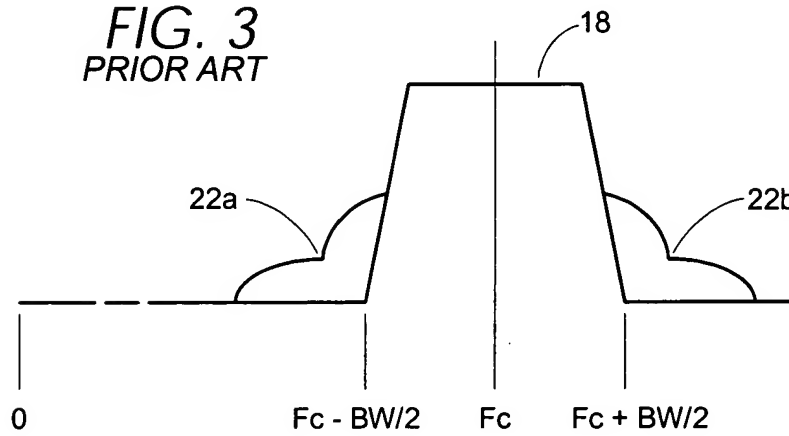


FIG. 1

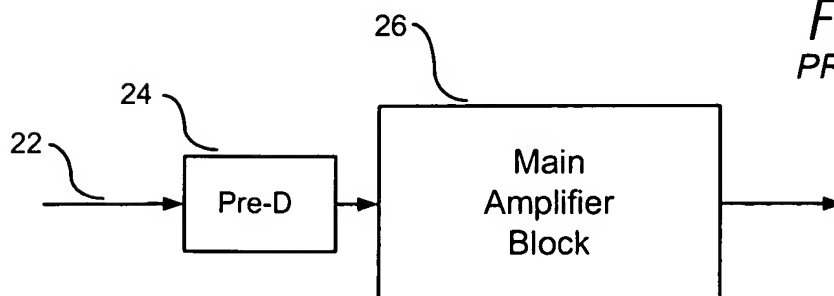
*FIG. 2*  
*PRIOR ART*



*FIG. 3*  
*PRIOR ART*



*FIG. 4*  
*PRIOR ART*



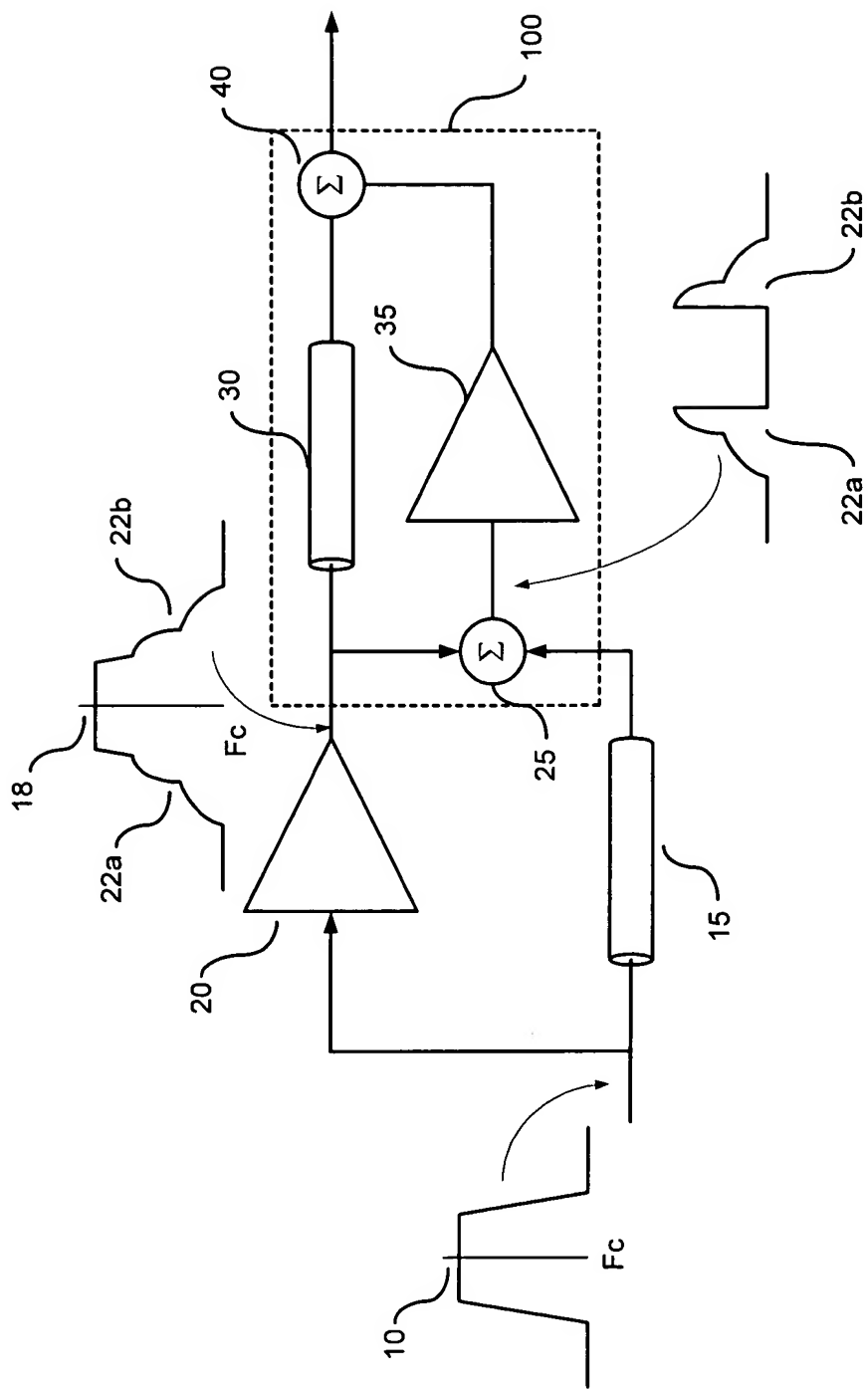
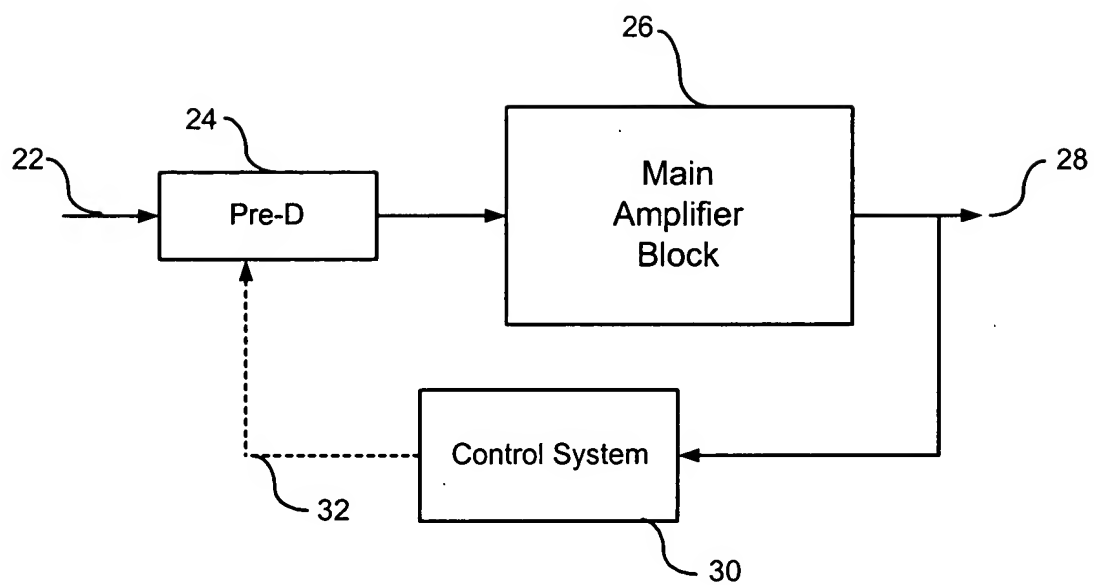


FIG. 5  
PRIOR ART



*FIG. 6*  
*PRIOR ART*

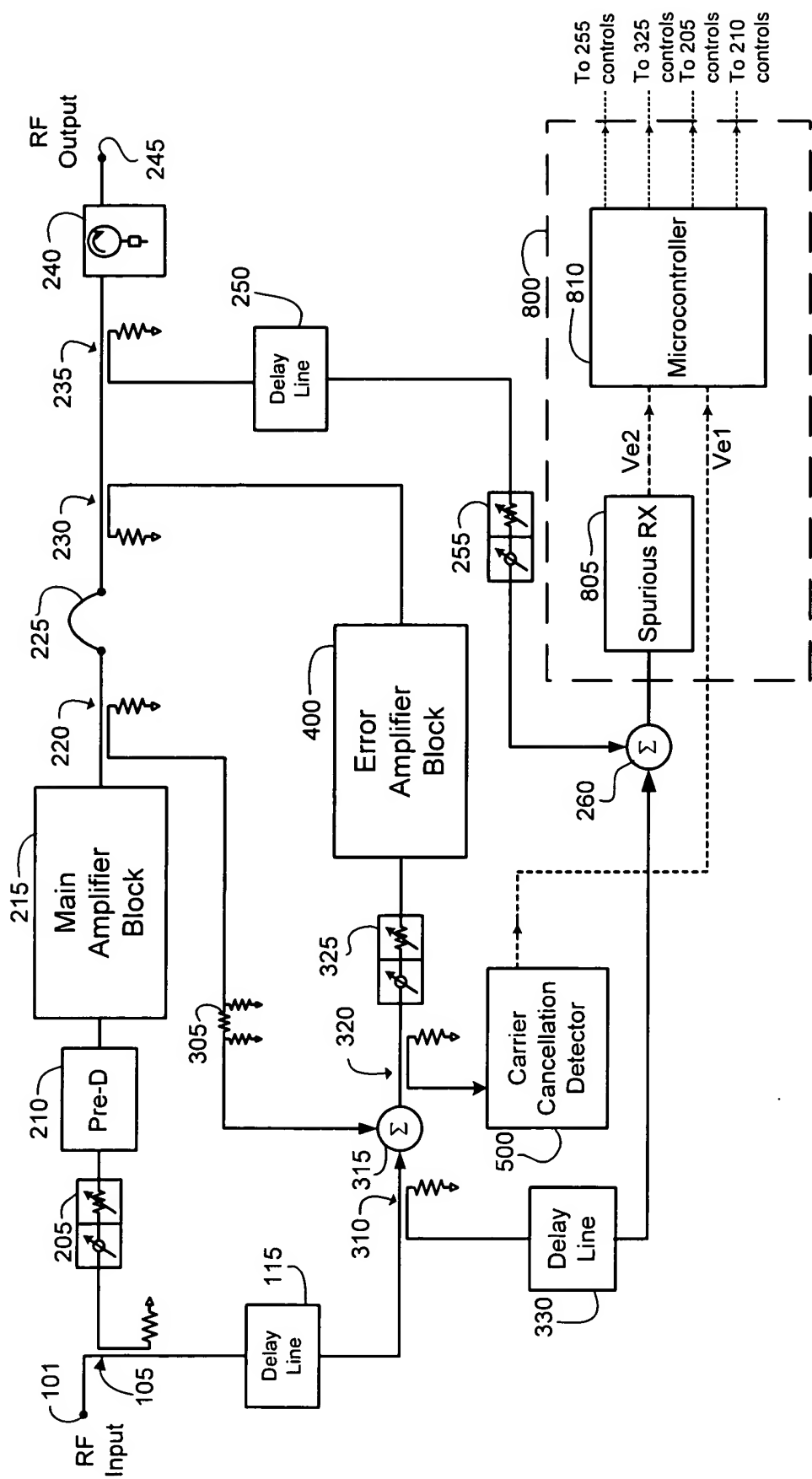


FIG. 7

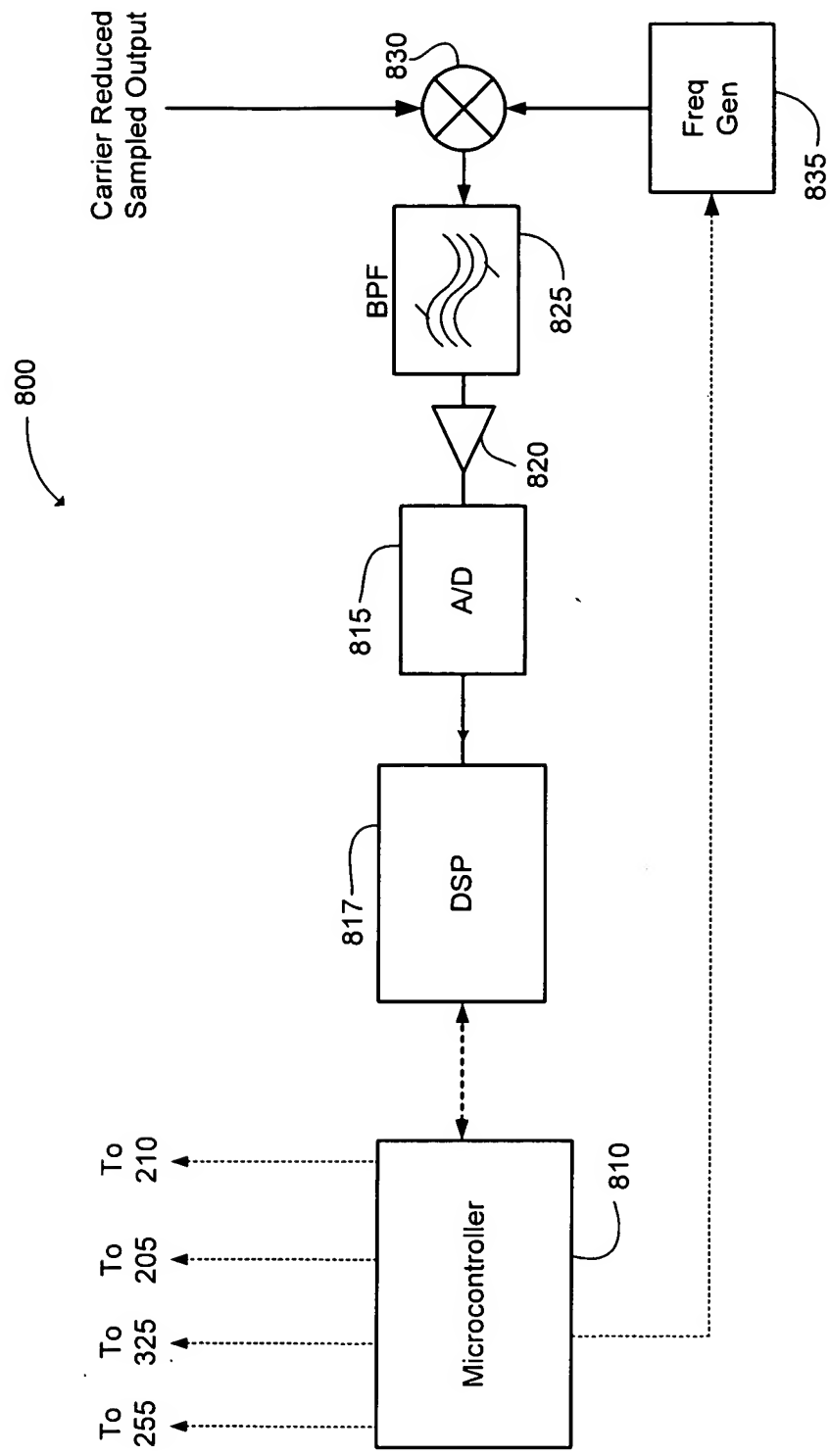


FIG. 8

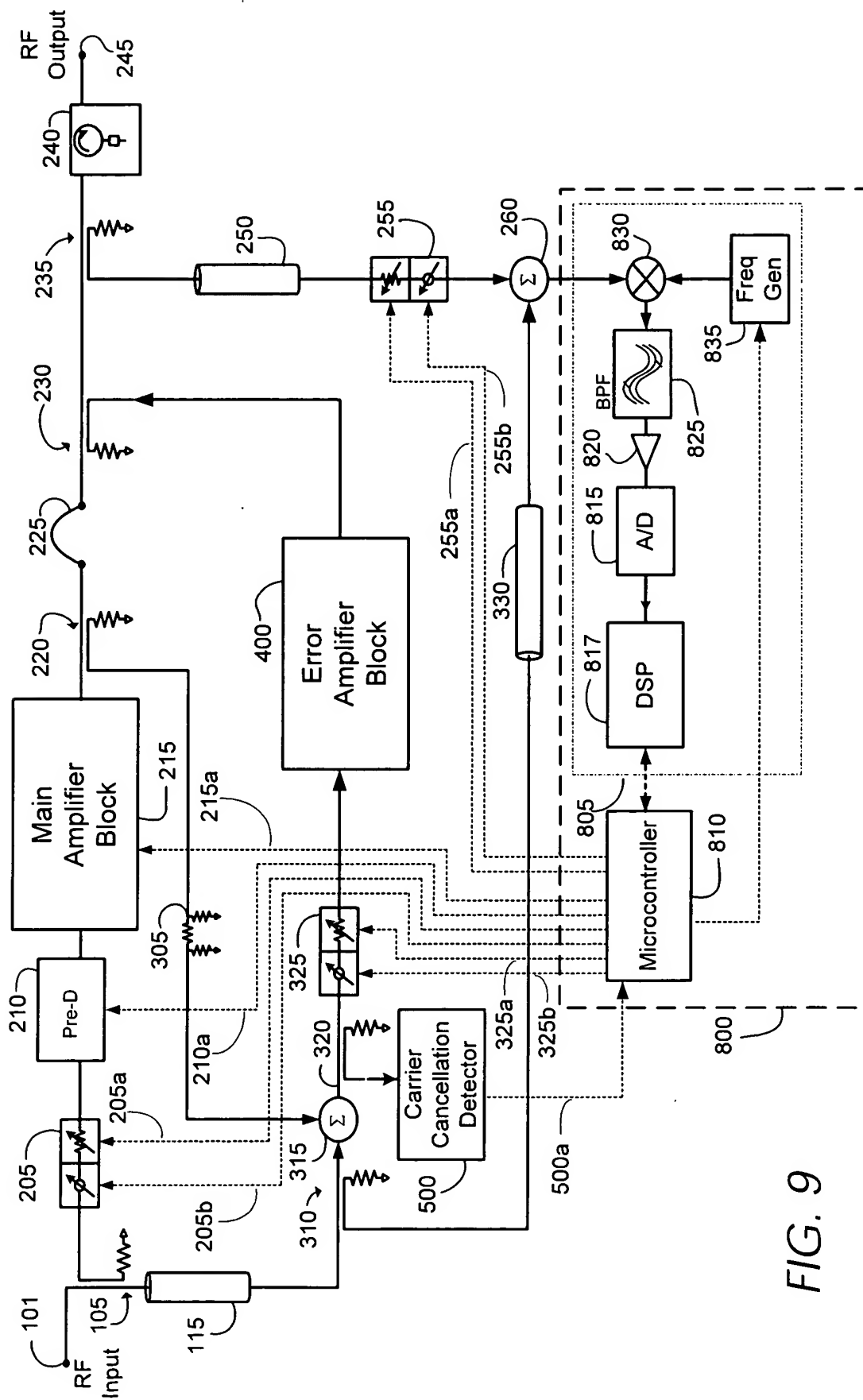


FIG. 9

FIG. 10

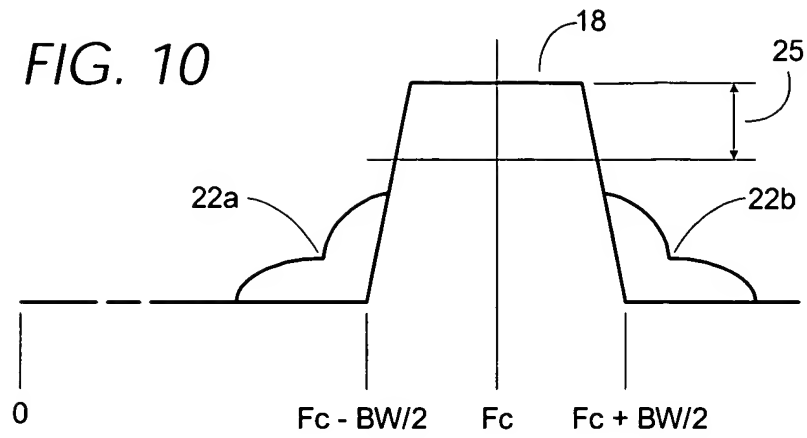


FIG. 11

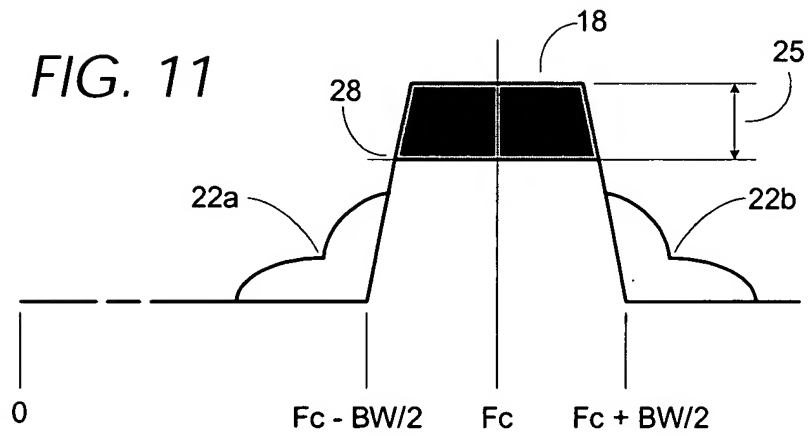
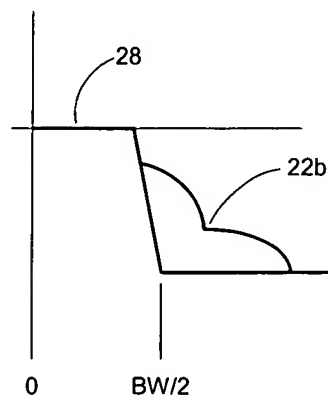


FIG. 12





Loop Cancellation BW vs. number of cycles  
of delay mismatch (N)

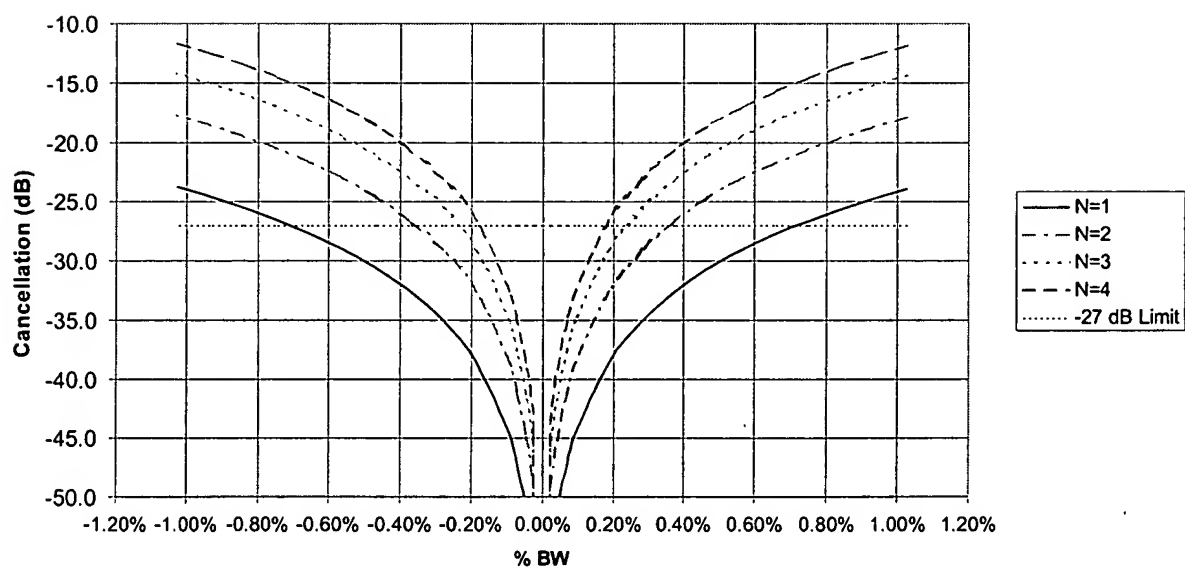
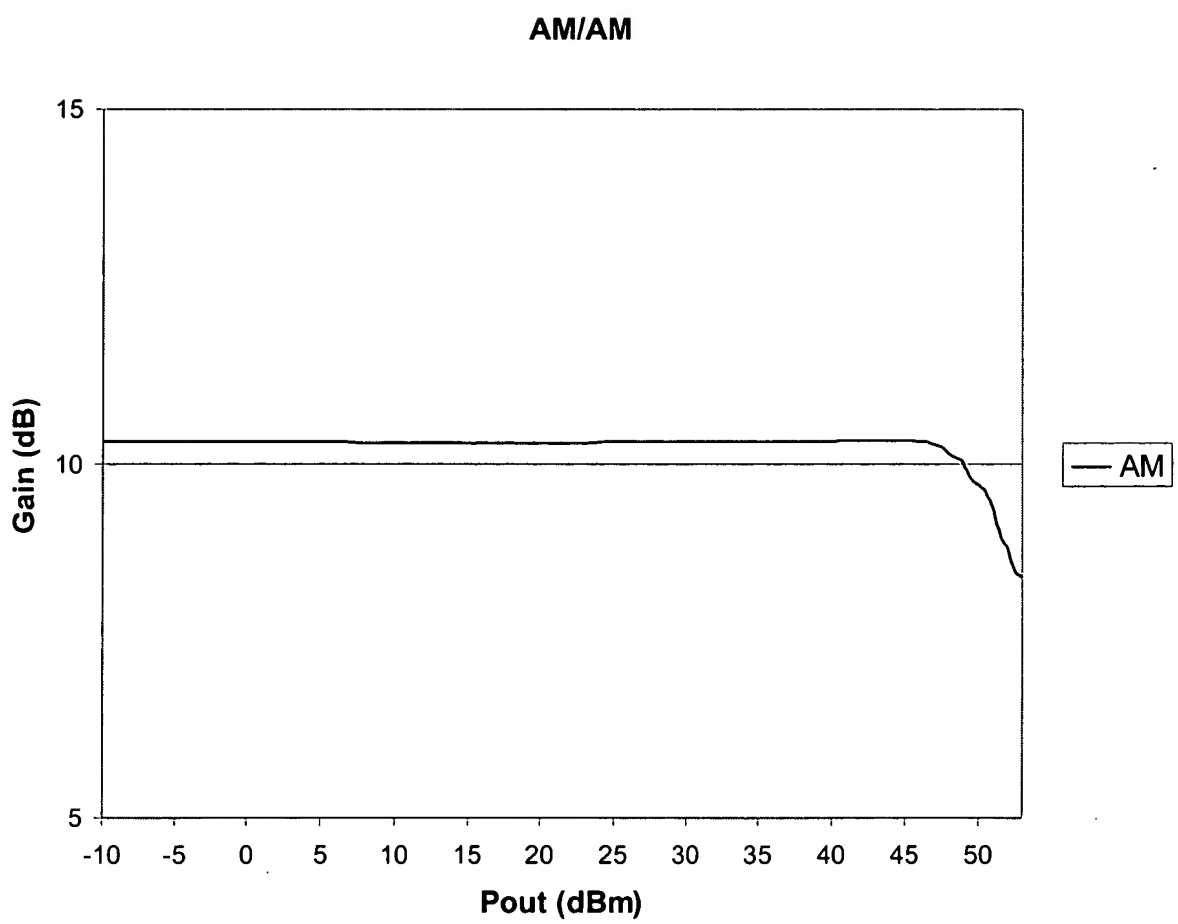


FIG. 13



*FIG. 14*

# AM/PM

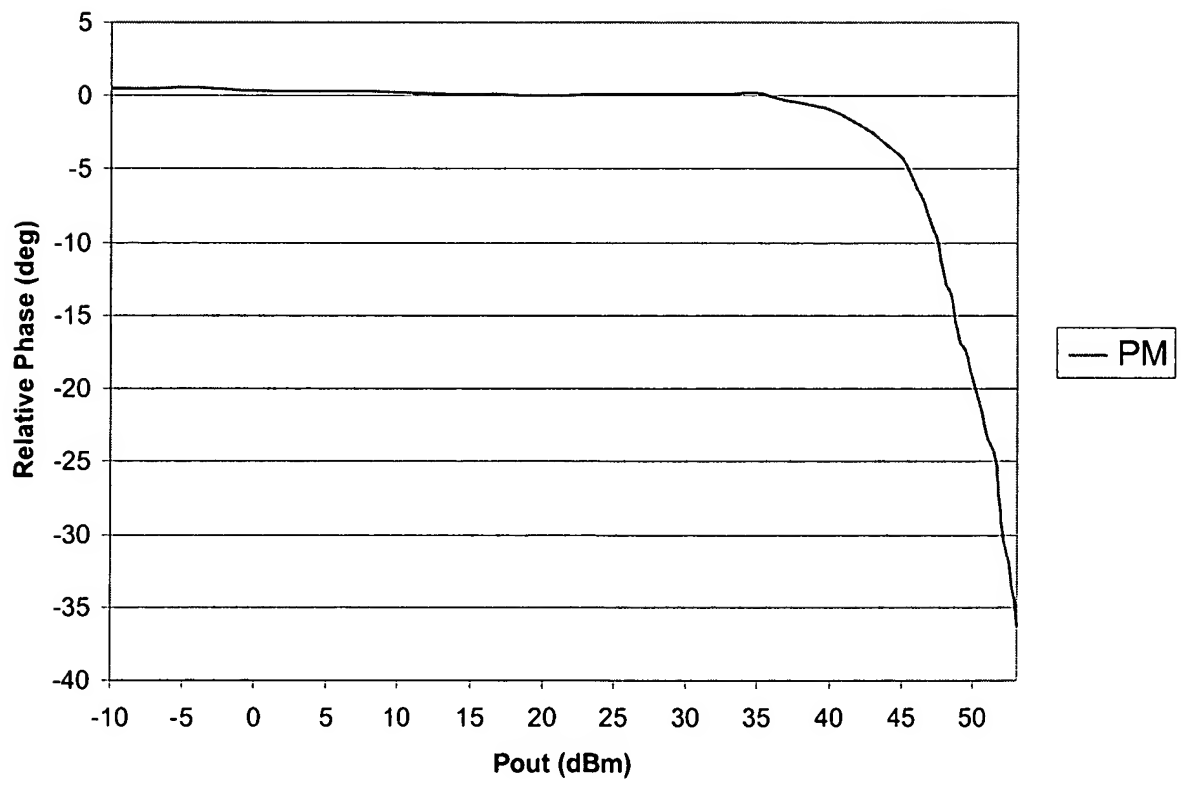


FIG. 15

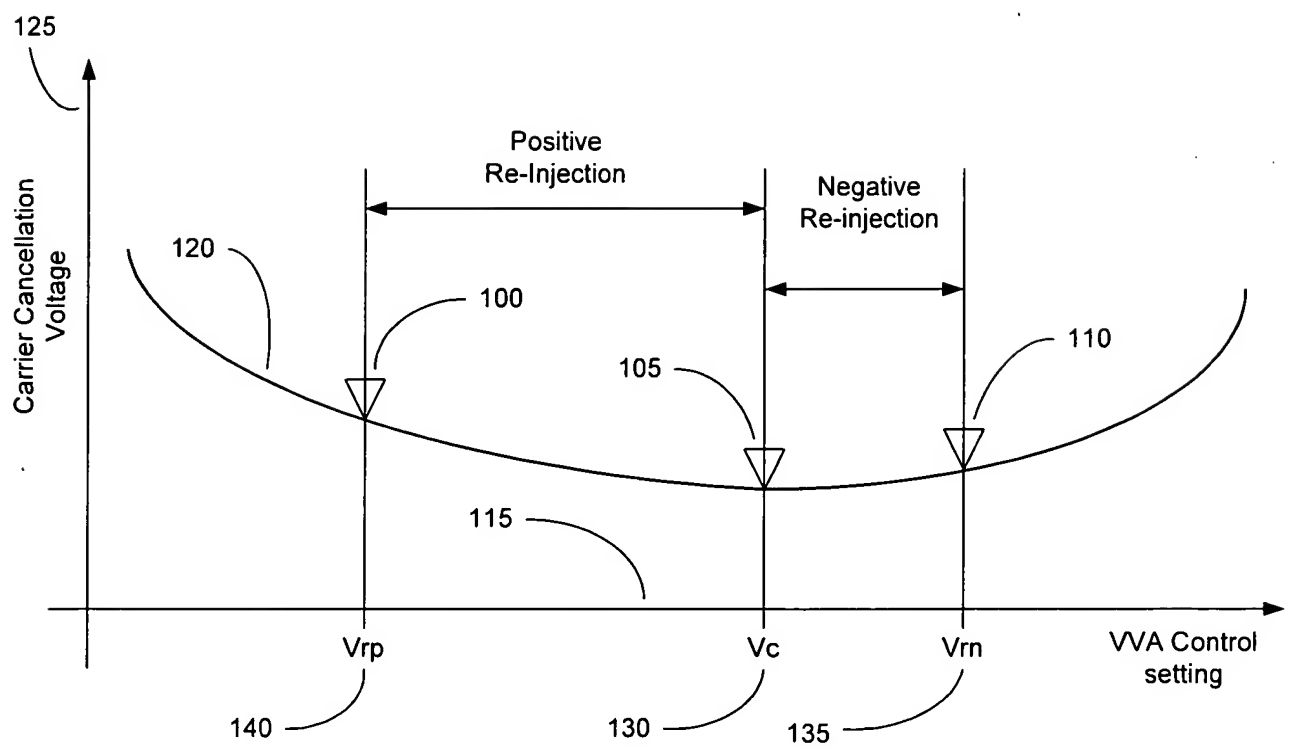


FIG. 16